



STIC Search Report

EIC 2100

STIC Database Tracking Number: 119240

TO: Insun Kang
Location: 4A17
Art Unit : 2124
Monday, April 12, 2004

Case Serial Number: 09/681263

From: Carol Wong
Location: EIC 2100
PK2-4B33
Phone: 305-9729

carol.wong@uspto.gov

Search Notes

Dear Examiner Kang,

Attached are the search results (from commercial databases) for your case.

Color tags mark the patents/articles which appear to be most relevant to the case. Color of tag has no significance. Pls review all documents, since untagged items might also be of interest. If you wish to order the complete text of any document, pls submit request(s) directly to the EIC2100 Reference Staff located in PK2-4B40.

Pls call if you have any questions or suggestions for additional terminology, or a different approach to searching the case. Finally, pls complete the attached Search Results Feedback Form, as the EIC/STIC is continually soliciting examiners' opinion of the search service.

Thanks,
Carol





STIC EIC 2100 Search Request Form

119240

Today's Date:

What date would you like to use to limit the search?

Priority Date:

Other:

Name Insun Kang

AU 2124 Examiner # 80013

Room # 4A17 Phone 305-6465

Serial # 09681263

Format for Search Results (Circle One):

PAPER DISK EMAIL

Where have you searched so far?

USP DWPI EPO JPO ACM IBM TDB

IEEE INSPEC SPI Other _____

Is this a "Fast & Focused" Search Request? (Circle One) YES NO

A "Fast & Focused" Search is completed in 2-3 hours (maximum). The search must be on a very specific topic and meet certain criteria. The criteria are posted in EIC2100 and on the EIC2100 NPL Web Page at <http://ptoweb/patents/stic/stic-tc2100.htm>.

What is the topic, novelty, motivation, utility, or other specific details defining the desired focus of this search? Please include the concepts, synonyms, keywords, acronyms, definitions, strategies, and anything else that helps to describe the topic. Please attach a copy of the abstract, background, brief summary, pertinent claims and any citations of relevant art you have found.

builder tool that has a multiple independent virtual machine
environment (parameters option)

STIC Searcher Candace

Phone 305-9729

Date picked up 4-12-04

Date Completed 4-12-04



PRODUCT DOCUMENTATION REQUEST FORM

Requester's Name: <u>Insun Kang</u>		Case Serial Number: <u>09681263</u>	Art Unit/Org.: <u>2124</u>
Phone: <u>305-6465</u>	Fax:	Building: <u>PK2</u>	Room Number: <u>4A17</u>
Class/Sub-Class: <u>717</u>			
Date of Request: <u>12/11/03</u>		Date Needed By:	
Paste or add text of citation or bibliography: Paste Citation		Submit only one request per form.	
Filing/Effective Date:	<u>1/16/01</u>		
Product Title:	<u>Jembuilder</u>		
Version:			
Company:	<u>AJILE Systems</u>		
Company Phone:	<u>408-557-0829</u>		
Company Location:	<u>CA</u>		
Remarks:			

Staff Use Only

Monthly Accession Number:

Action	Researcher	Contact Name/Phone	Outcome of Effect	Follow-up
Date Completed:		Remarks/Comments:		

The screenshot shows the aJile Systems website. The navigation menu on the left includes: Home, Products (selected), Support, Company, Contact Us, Careers, Papers, and Partners. The main content area is titled "Embedded Low Power Java Microprocessors" and lists application areas: Cell Phones, Set Top Boxes, Autos, Internet based Controllers, and PDA's. The product information section is titled "Real-time Java™ Single Board Computer aJ-100EVB™".

IP:

- aJ-100WRP
- JEMCore

Processor Families:

- aJ-100
- aJ-80

Development Systems:

- aJ-100EVB
- aJ-PC104

Real-time Java™ Single Board Computer aJ-100EVB™

The aJ-100EVB is a single board computer based on the aJ-100 processor. It enables our customers to quickly evaluate our processor technology. It also provides an ideal platform for embedded Java™ software development.

The aJ-100EVB is also available in a SDRAM based version, the aJ-100EVBm. The regular aJ-100EVB has SRAM memory and the aJ-100EVBm has SDRAM memory. Because SDRAM is more cost effective, but somewhat slower than the SRAM, the two boards provide options for either maximal performance or large memory capacity. **Note: The aJ-100EVB has been discontinued! Equivalent products can be purchased at Systronix and Aidonic.**

The aJ-100EVB/m comes bundled with our Java™ Runtimes and development tools including JEMBuilder™ and Charade debugger. A Sun Microsystems J2ME/CLDC™ license is included along with these features:

Key Features:

- aJ-100 Java direct execution processor.
- 1 MB Fast SRAM (aJ-100EVB) or 16 MB SDRAM (aJ-100EVBm).
- 4 MB Flash.
- IEEE 1284 interface to aJ-100 JTAG port.
- 2 Serial Ports.
- SPI Port with 3 (4 if touch screen not used) available select lines.
- 4-wire resistive touch screen controller.
- Epson S1D13706 Embedded Memory LCD Controller
- Cirrus Logic CS8900 10Base-T Ethernet controller.
- 13 GPIOs (8 with +/- 24 mA drive).

[Datasheet of the aJ-100EVB \(PDF\)](#)

[Schematic of the aJ-100EVB \(PDF\)](#)

Price:

aJ-100EVB DISCONTINUED!

aJ-100EVBm US\$1,499.-.

Availability: The aJ-100EVB has been discontinued, the aJ-100EVBm is available now!

How to buy:

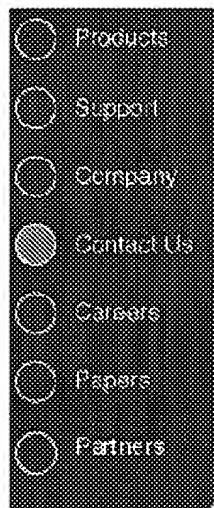
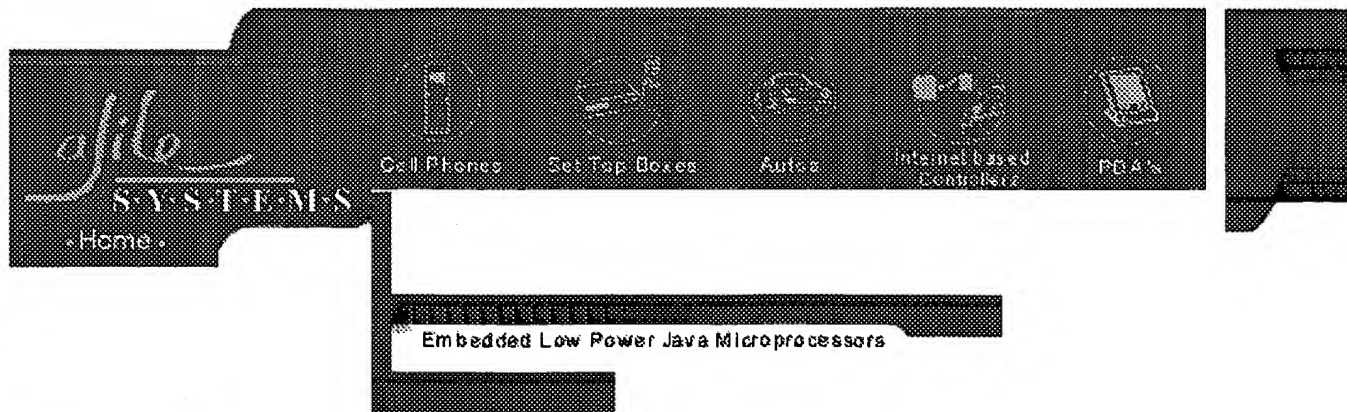
Orders for aJ-100EVBm are accepted via fax at 408-557-8279 and via email to sales@ajile.com in the USA. Credit card purchase is preferred. In other countries, please place your order through our [international distributors and representatives](#)

Customers to add an LCD panel to the aJ-100EVB may want to consider a clone manufactured by [AIDONIC Corporation, Japan](#) that includes a built-in LCD panel. Please contact Aidonic Corp. directly for pricing and availability of its [Real Time Java Panel Computer JPC1001](#).

For more information about the aJ-100EVB, please go to our [Technical FAQ](#) pages.

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Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 09681263 **Filing Dt:** 03/09/2001**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** 20020095396**Pub Dt:** 07/18/2002**Inventors:** Michael J. Frerking, David S. Hardin, Nick M. Mykris, Philip J. Wiley**Title:** Builder tool and interface for system supporting multiple virtual machines**Assignment: 1****Reel/Frame:** 012133/0195 **Received:**
09/10/2001**Recorded:**
09/04/2001**Mailed:**
11/07/2001**Pages:**
5**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** FRERKING, MICHAEL J.**Exec Dt:** 08/21/2001HARDIN, DAVID S.**Exec Dt:** 08/21/2001MYKRIS, NICK M.**Exec Dt:** 08/21/2001WILEY, PHILIP J.**Exec Dt:** 08/21/2001**Assignee:** AJILE SYSTEMS, INC.

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SAN JOSE, CALIFORNIA 95129

Correspondent: SIMMONS, PERRINE, ALBRIGHT & ELLWOOD, PLC

MR. MICHAEL WILLIAMS

115 THIRD STREET S.E.

SUITE 1200

CEDAR RAPIDS, IA 52401-1266

Search Results as of: 12/11/2003 2:14:47 P.M.

If you have any comments or questions concerning the data displayed, contact OPR / Assignments at 703-308-9723
Web interface last modified: Oct. 5, 2002

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Set	Items	Description
S1	30884	VIRTUAL(1W)MACHINE? ? OR VM OR VMS OR JVM OR JVMS
S2	287	(PLURALIT??? ? OR MULTIPLE OR MANY OR MULTI OR MULTIPLICIT? OR SEVERAL OR NUMEROUS OR MULTITUD? OR PLURIF? OR SECOND OR - BOTH) (1W)S1
S3	305	(VARIOUS OR VARIETY OR GROUP??? ? OR CLUSTER? OR NUMBER OR PAIR???? ? OR SET? ? OR NETWORK? OR CHAIN? ? OR SERIES OR ANO- THER) (1W)S1
S4	100	(DUAL OR THREE OR TWO OR RANGE) (1W)S1
S5	32	ALL(1W)S1
S6	103	(EACH OR INDIVIDUAL? OR SEPARATE?) (1W)S1
S7	15087185	STATUS? OR CONDITION? ? OR MODE OR MODES OR PROPERTY? OR P- ROPERTIES OR PARAMETER? OR PARAMETRE? OR FEATURE OR FEATURES - OR ATTRIBUTE OR ATTRIBUTES
S8	9058093	STATE OR STATES
S9	645598	SITUATION? ? OR CIRCUMSTANCE
S10	2923227	CLASSPATH? OR PATH? ? OR PATHWAY? OR MEMORY OR CLASSES OR - CLASS
S11	175107	S7:S10(3N) (DISPLAY? OR VIEW??? ? OR VISUAL? OR VISUALIS? OR VISUALIZ? OR GRAPHIC???? ? OR ONSCREEN? OR SCREEN? OR VDT? ? OR CRT? ? OR LCD? ? OR VDU? ? OR VIEWSCREEN? OR VIEWPORT?)

S12 741344 S7:S10(3N) (LOOK??? ? OR SEE OR SEES OR SEEING OR SEEN OR S-
EEABLE OR SHOW??? ? OR VISIB? OR OBSERVE? ? OR OBSERVAB? OR V-
IDEO OR APPEAR?)
S13 21701 S7:S10(3N) PRESENTATION?
S14 16463 S7:S10(3N) (LED OR LEDS)
S15 17 S2:S6 AND S11:S14
S16 2 S15/2001:2004
S17 15 S15 NOT S16
S18 11 RD (unique items)

18/7/10 (Item 1 from file: 233)
DIALOG(R) File 233: Internet & Personal Comp. Abs.
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00348760 94PI05-289

Adventures in flatland with VxDs

Prosis, Jeff

PC Magazine , May 31, 1994 , v13 n10 p291-297, 4 Page(s)

ISSN: 0888-8507

Company Name: Jeff Prosis

Product Name: VTUTORD.ASM

TUTOR column presents an assembly language program for IBM PC compatibles called VTUTORD.ASM, illustrating how a simple VxD (virtual device driver) allows a DOS application to determine the **number** of **virtual machines** in the system. Explains in detail how VxDs can be used to program the operating system, where once installed, a VxD becomes part of the Virtual Machine Manager and can thus **look** anywhere in **memory**. Claims that VxDs are powerful, 32-bit programs that extend the Windows OS. Notes that VxDs run in ring 0, which is the processor's most privileged section, sometimes called flatland, and can reach any address within a 4GB address space without altering the segment registers, and execute privileged processor instructions. States that most system VxDs are used to virtualize hardware accesses in a multitasking environment, or virtualize software resources. Covers ways of activating VxDs. Includes two program listings and one table. (jo)

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File 674:Computer News Fulltext 1989-2004/Apr W1
(c) 2004 IDG Communications

Set	Items	Description
S1	42063	VIRTUAL(1W)MACHINE? ? OR VM OR VMS OR JVM OR JVMS
S2	542	(PLURALIT??? ? OR MULTIPLE OR MANY OR MULTI OR MULTIPLICIT? OR SEVERAL OR NUMEROUS OR MULTITUD? OR PLURIF? OR SECOND OR - BOTH) (1W)S1
S3	510	(VARIOUS OR VARIETY OR GROUP??? ? OR CLUSTER? OR NUMBER OR PAIR???? ? OR SET? ? OR NETWORK? OR CHAIN? ? OR SERIES OR ANO- THER) (1W)S1
S4	155	(DUAL OR THREE OR TWO OR RANGE) (1W)S1
S5	197	ALL(1W)S1
S6	184	(EACH OR INDIVIDUAL? OR SEPARATE?) (1W)S1
S7	8944712	STATUS? OR CONDITION? ? OR MODE OR MODES OR PROPERTY? OR P- ROPERTIES OR PARAMETER? OR PARAMETRE? OR FEATURE OR FEATURES - OR ATTRIBUTE OR ATTRIBUTES
S8	14356861	STATE OR STATES
S9	2377277	SITUATION? ? OR CIRCUMSTANCE
S10	3417153	CLASSPATH? OR PATH? ? OR PATHWAY? OR MEMORY OR CLASSES OR - CLASS
S11	228146	S7:S10(3N) (DISPLAY? OR VIEW??? ? OR VISUAL? OR VISUALIS? OR VISUALIZ? OR GRAPHIC???? ? OR ONSCREEN? OR SCREEN? OR VDT? ? OR CRT? ? OR LCD? ? OR VDU? ? OR VIEWSCREEN? OR VIEWPORT?)
S12	641450	S7:S10(3N) (LOOK??? ? OR SEE OR SEES OR SEEING OR SEEN OR S- EEABLE OR SHOW??? ? OR VISIB? OR OBSERVE? ? OR OBSERVAB? OR V- IDEO OR APPEAR?)
S13	26329	S7:S10(3N) PRESENTATION?
S14	67750	S7:S10(3N) (LED OR LEDS)
S15	22	S2:S6(S)S11:S14

S16 10 S15/2001:2004
S17 12 S15 NOT S16
S18 11 RD (unique items)

18/3,K/1 (Item 1 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2004 ProQuest Info&Learning. All rts. reserv.

00564677 91-39031
Cabletron: Multivendor Management Made Real
Anonymous
Datamation v37n16 PP: 67 Aug 15, 1991
ISSN: 0011-6963 JRNL CODE: DAT

...ABSTRACT: of perspectives. Unique to Spectrum is the artificial intelligence-based inductive-modeling technology underlying its **Virtual Network Machine** (VNM) software. A modeling engine turns network data into reports and **views** of device **status** and also of LANs or subnets whose performance or status can be measured only by...

18/3,K/2 (Item 2 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
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00443753 89-15540
Improving VM Performance
Eddolls, Trevor
Computerworld v23n13 PP: 87-94 Mar 27, 1989
ISSN: 0010-4841 JRNL CODE: COW

...ABSTRACT: be in several states, Bernard McIlroy of Progress Lighting Inc. wrote an assembler program that **shows** the current **state** of any given VM. The program also shows how much real storage is being used...

... Kauf-Stern of Paz Oil Co. wrote a number of executable files that automatically monitor **all** the **VMs** and their usage of such resources as CPU utilization, input-output rate, and paging rate...
?

File 9:Business & Industry(R) Jul/1994-2004/Apr 09
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Set	Items	Description
S1	85867	VIRTUAL(1W)MACHINE? ? OR VM OR VMS OR JVM OR JVMs
S2	1570	(PLURALIT??? ? OR MULTIPLE OR MANY OR MULTI OR MULTIPLICIT? OR SEVERAL OR NUMEROUS OR MULTITUD? OR PLURIF? OR SECOND OR - BOTH) (1W)S1
S3	1136	(VARIOUS OR VARIETY OR GROUP??? ? OR CLUSTER? OR NUMBER OR PAIR???? ? OR SET? ? OR NETWORK? OR CHAIN? ? OR SERIES OR ANO- THER) (1W)S1
S4	379	(DUAL OR THREE OR TWO OR RANGE) (1W)S1
S5	629	ALL(1W)S1
S6	656	(EACH OR INDIVIDUAL? OR SEPARATE?) (1W)S1
S7	9457296	STATUS? OR CONDITION? ? OR MODE OR MODES OR PROPERTY? OR P- ROPERTIES OR PARAMETER? OR PARAMETRE? OR FEATURE OR FEATURES - OR ATTRIBUTE OR ATTRIBUTES
S8	20863329	STATE OR STATES
S9	1254039	SITUATION? ? OR CIRCUMSTANCE
S10	3302161	CLASSPATH? OR PATH? ? OR PATHWAY? OR MEMORY OR CLASSES OR - CLASS
S11	338586	S7:S10(3N) (DISPLAY? OR VIEW??? ? OR VISUAL? OR VISUALIS? OR VISUALIZ? OR GRAPHIC???? ? OR ONSCREEN? OR SCREEN? OR VDT? ? OR CRT? ? OR LCD? ? OR VDU? ? OR VIEWSCREEN? OR VIEWPORT?)
S12	565138	S7:S10(3N) (LOOK??? ? OR SEE OR SEES OR SEEING OR SEEN OR S- EEABLE OR SHOW??? ? OR VISIB? OR OBSERVE? ? OR OBSERVAB? OR V- IDEO OR APPEAR?)
S13	31478	S7:S10(3N)PRESENTATION?
S14	46408	S7:S10(3N) (LED OR LEDS)
S15	69	S2:S6(S)S11:S14
S16	3	S15/2001:2004
S17	66	S15 NOT S16
S18	45	RD (unique items)

17/3,K/3 (Item 1 from file: 16)
 DIALOG(R)File 16:Gale Group PROMT(R)
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07917680 Supplier Number: 65351854 (USE FORMAT 7 FOR FULLTEXT)
**IMSYS HEDGES BETS ON JAVA : Rewritable-Microcode Chip Has Instruction Sets
 for Java, Forth, C/C++. (Imsys Cjip) (Product Announcement)**
 Halfhill, Tom R.
 Microprocessor Report, v14, n8, p36
 August, 2000
 Language: English Record Type: Fulltext
 Article Type: Product Announcement
 Document Type: Newsletter; Trade
 Word Count: 2648

... implements a garbage collector in microcode, but with a further

refinement: the aJ100 can run **multiple JVMs** as independent processes and allow each one to **observe** different rules for **memory** management. One of those JVMs can guarantee real-time response times by disabling the garbage...

17/3,K/4 (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
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07917679 Supplier Number: 65351853 (USE FORMAT 7 FOR FULLTEXT)
EMBEDDED JAVA CHIPS GET REAL : Bytecode-Native aJ-100 Handles Real-Time Processing. (AJile's AJile aJ-100 microprocessor) (Product Announcement)
Halfhill, Tom R.
Microprocessor Report, v14, n8, p31
August, 2000
Language: English Record Type: Fulltext
Article Type: Product Announcement
Document Type: Newsletter; Trade
Word Count: 3192

... with programs that require fast context switching to handle interrupts in real time.

By running **multiple JVMs** as separate processes, the aJ-100 allows software developers to assign different tasks to different JVMs. Moreover, different JVMs can **observe** different rules for **memory** management. The JVM dedicated to real-time processing can disable automatic garbage collection altogether, while other tasks that don't require realtime response can run on **another JVM** that has a normal garbage collector.

Partitioning an application across multiple JVMs is also a...

17/3,K/10 (Item 8 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
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01350704 Supplier Number: 41597907
MultiSessions 2.0 ANNOUNCED
News Release, p1
Oct 8, 1990
Language: English Record Type: Abstract
Document Type: Magazine/Journal; Trade

ABSTRACT:

...by allowing him to create and switch back and forth between up to 10 completely **separate VAX/ VMS** processes all running from the same terminal. Popular with both developers and end users, MultiSessions...

...when he was last connected to that session. The screen repaint also restores the proper **screen attributes** (such as **screen width**) so that output will display correctly.

...
?t17/3,k/16-17,30

17/3,K/16 (Item 4 from file: 47)
DIALOG(R)File 47:Gale Group Magazine DB(TM)
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03709178 SUPPLIER NUMBER: 12069188 (USE FORMAT 7 OR 9 FOR FULL TEXT)
OS/2 2.0 does the job: IBM has fixed many of the problems that plagued earlier versions of OS/2. (IBM's OS/2 2.0 operating system) (includes related articles on purchasing criteria, on the need for powerful OS/2-compliant applications and on the Workplace Shell) (Software Review) (Evaluation)
Smith, Gina
PC-Computing, v5, n5, p48(8)
May, 1992

DOCUMENT TYPE: Evaluation ISSN: 0899-1847 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 2461 LINE COUNT: 00185

... was the first to offer Virtual 8086 mode. In this mode, the processor sets up **multiple virtual DOS machines** (VDMs), which are areas in **memory** that **appear** to applications as standalone DOS PCs. Each VDM has its own output and peripheral resources...

17/3,K/17 (Item 5 from file: 47)
DIALOG(R)File 47:Gale Group Magazine DB(TM)
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03080829 SUPPLIER NUMBER: 06161466 (USE FORMAT 7 OR 9 FOR FULL TEXT)
VM-386 multitasks existing DOS applications far beyond 640K. (Software Review) (evaluation)

Simon, Barry
PC Magazine, v7, n2, p33(3)
Jan 26, 1988

DOCUMENT TYPE: evaluation ISSN: 0888-8507 LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 993 LINE COUNT: 00075

... total RAM. While the kernel doesn't take anything away from the 640K maximum that **each VM** can have, it does take 640K from the total RAM pool. In addition to its DOS memory (which can be less than 640K if you wish), **each VM** needs **memory** to virtualize the **video** (up to 128K for standard EGA support).

At any time, the SysReq key brings up...

17/3,K/30 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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02442150 SUPPLIER NUMBER: 65351854 (USE FORMAT 7 OR 9 FOR FULL TEXT)
IMSYS HEDGES BETS ON JAVA : Rewritable-Microcode Chip Has Instruction Sets for Java, Forth, C/C++. (Imsys Cjip) (Product Announcement)

Halfhill, Tom R.
Microprocessor Report, 14, 8, 36
August, 2000

DOCUMENT TYPE: Product Announcement ISSN: 0899-9341 LANGUAGE: English
RECORD TYPE: Fulltext
WORD COUNT: 2874 LINE COUNT: 00229

... implements a garbage collector in microcode, but with a further refinement: the aJ100 can run **multiple JVMs** as independent processes and allow each one to **observe** different rules for **memory** management. One of those JVMs can guarantee real-time response times by disabling the garbage...

?t17/3,k/31,33,35,37,40,46

17/3,K/31 (Item 2 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2004 The Gale Group. All rts. reserv.

02442149 SUPPLIER NUMBER: 65351853 (USE FORMAT 7 OR 9 FOR FULL TEXT)
EMBEDDED JAVA CHIPS GET REAL : Bytecode-Native aJ-100 Handles Real-Time Processing. (AJile's AJile aJ-100 microprocessor) (Product Announcement)

Halfhill, Tom R.
Microprocessor Report, 14, 8, 31
August, 2000

DOCUMENT TYPE: Product Announcement ISSN: 0899-9341 LANGUAGE: English
RECORD TYPE: Fulltext
WORD COUNT: 3511 LINE COUNT: 00278

... with programs that require fast context switching to handle

interrupts in real time.

By running **multiple JVMs** as separate processes, the aJ-100 allows software developers to assign different tasks to different JVMs. Moreover, different JVMs can **observe** different rules for **memory** management. The JVM dedicated to real-time processing can disable automatic garbage collection altogether, while other tasks that don't require realtime response can run on **another JVM** that has a normal garbage collector.

Partitioning an application across multiple JVMs is also a...

17/3,K/33 (Item 4 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

02243173 SUPPLIER NUMBER: 20966251 (USE FORMAT 7 OR 9 FOR FULL TEXT)

SYMANTEC UNVEILS VISUAL JAVA ENTERPRISE SUITE.

Computergram International, n150, pCGN07290006

July 29, 1998

ISSN: 0268-716X LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 551 LINE COUNT: 00046

TEXT:

...suggests that the new Cafe can do just that. One window in the interface lists **all the virtual machines** participating in a given application. Developers can debug each thread in their software, no matter ...

...puts it: "Once you have the tools the whole thing becomes more of a Lego **situation** ." **Visual** Cafe for Java Enterprise Suite supports the usual suspects as far as middle-tier application...

17/3,K/35 (Item 6 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01676039 SUPPLIER NUMBER: 15082011 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Q & A MS-DOS. (Column)

Prosise, Jeff

Microsoft Systems Journal, v9, n5, p77(6)

May, 1994

DOCUMENT TYPE: Column ISSN: 0889-9932 LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT

WORD COUNT: 2292 LINE COUNT: 00175

... instruction. As a result of this effort, the 4-byte area that VDMD established in **each VM** 's control block stores a code that indicates the VM **status** -windowed or full **screen** --at all times.

The final piece of the puzzle is the procedure VDMD[underscore]API...

17/3,K/37 (Item 8 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01664035 SUPPLIER NUMBER: 15005226 (USE FORMAT 7 OR 9 FOR FULL TEXT)

R:Base 4.5: the stealth upgrade. (Microrim Inc.'s relational data base management system) (Software Review) (Desktop Developer) (Evaluation)

Acker, Bob

DBMS, v7, n1, p83(3)

Jan, 1994

DOCUMENT TYPE: Evaluation ISSN: 1041-5173 LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 1991 LINE COUNT: 00155

... the other two are R:Base 4.5 for OS/2. All sessions act as **separate virtual machines** . The DOS sessions require separate network logins to connect to the server database; the OS...

...row of data, changing and saving that data in any session updates all the other **displays** transparently.

Rational Memory

R:Base 4.5 runs as a protected-mode DOS application under Windows 3.1

...

17/3,K/40 (Item 11 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01581488 SUPPLIER NUMBER: 13341820 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Call VxD functions and VMM services easily using our generic VxD. (Virtual Device Adaption Guide that comes with the Windows Device Driver Kit)

(Virtual Device Drivers, Virtual Machine Manager) (Technical)

Schulman, Andrew

Microsoft Systems Journal, v8, n2, p17(21)

Feb, 1993

DOCUMENT TYPE: Technical ISSN: 0889-9932

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 8217 LINE COUNT: 00633

... vmwalk function as VMINFO to enumerate VM handles, DPMI[underscore]MEM.C (see Figure 22) shows the DPMI memory allocations for each VM. The DPMI Allocate Memory Block function (INT 31H AX=0501H) returns both the linear address...21H AH=62H) over and over in the System VM, in both V86 and protected **mode**. The GetVMPSP function **shown** in Figure 26 uses VMInt86 to execute an INT 21H AH=62H in a specified...

...code for getting a program name from a PSP, together with the rules for accessing **another VM** 's V86 data in the second part of Figure 9, can be used to get...

...executing program (or at least the name of the program with the current PSP) in **each VM**. This should probably be added to VMINFO.

As you can see, using VMInt86 is not...

17/3,K/46 (Item 17 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01370200 SUPPLIER NUMBER: 08674482 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Inside Windows 3. (Microsoft Corp.'s Windows 3.0 graphical user interface) (Windows 3) (technical)

Morris, Peter

EXE, v5, n2, p12(4)

July, 1990

DOCUMENT TYPE: technical ISSN: 0268-6872

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 2734 LINE COUNT: 00204

... you're using a 386/486, each DOS session/application can be run in a **separate virtual 8086 machine**. These applications may be as badly behaved as they like; for example, they may attempt to write directly to **video memory**. Under enhanced **mode**, text mode DOS applications may even be run in a window. As far as the...

...also be used, if the application knows what to do). Attempts to write directly to **video memory** are trapped. Windows runs a 'text mode emulator' in the window it has provided for the application (Windows itself, of course, runs in **graphics mode**), and it's here (and only here) that the application's output may appear. The...
?t17/3,k/52-53,55-56,60-61,64

17/3,K/52 (Item 23 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01252617 SUPPLIER NUMBER: 06757957 (USE FORMAT 7 OR 9 FOR FULL TEXT)

VM:386 1.01. (Software Review) (Product watch: reviews and updates)

(evaluation)

Shields, Jim

PC Tech Journal, v6, n7, p123(4)

July, 1988

DOCUMENT TYPE: evaluation ISSN: 0738-0194

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 2128 LINE COUNT: 00168

...ABSTRACT: features include: ability to configure a start-up file, easy switching, full-screen video for each virtual machine application, detailed status information, menus to activate or deactivate specific machines, and main menu to control **screen attributes** or performance options. Help information is available and the program is well-documented. The software...

17/3,K/53 (Item 24 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01242841 SUPPLIER NUMBER: 06537959 (USE FORMAT 7 OR 9 FOR FULL TEXT)

EMS support improves Microsoft Windows 2.0 application performance.

(Expanded Memory Specification)

Yao, Paul

Microsoft Systems Journal, v3, n1, p57(10)

Jan, 1988

ISSN: 0889-9932 LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 5021 LINE COUNT: 00393

... is its support of standard MS-DOS applications. The current implementation of Windows/386 creates **multiple virtual machines** (VMs), each of which behaves as an independent 8086. Because each is effectively given its...

...with each other. Also, because of the ability of the Intel 80386 to intercept direct **memory** mapped **video** output, each MS-DOS application can run in its own window on the screen.

Note...

17/3,K/55 (Item 26 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01205834 SUPPLIER NUMBER: 04655430 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Upward to the 80386. (Intel's 80386 microprocessor architecture)

Crosswy, Caldwell; Perez, Mike

PC Tech Journal, v5, n2, p50(11)

Feb, 1987

ISSN: 0738-0194 LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 8316 LINE COUNT: 00671

... to common routines and data, such as a system ROM, by making the physical ROM **appear** in the **memory** space of each simulated machine. Actually, only one ROM exists, but each machine sees it...

...address within its 1MB address space. Figure 9 shows how the 80386 paging mechanism enables **multiple virtual -8086 machines** to be managed; a single copy of the 8086/88 operating system is made to...

17/3,K/56 (Item 27 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

01177185 SUPPLIER NUMBER: 04322091 (USE FORMAT 7 OR 9 FOR FULL TEXT)

Microsoft designing Windows version for use with 80386.

Kanzler, Stephen

PC Week, v3, n34, p1(2)

Aug 26, 1986

ISSN: 0740-1604

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 617

LINE COUNT: 00049

...ABSTRACT: virtual 86 mode to create a 640Kbyte virtual machine for each application being run, with **each virtual machine** appearing to the applications as an individual personal computer. This allows each application to run...

...with the Windows machine coordinating the activities of each and allocating computer resources, such as **memory**, the **display** and other I-O devices. Microsoft would not comment on the reports other than to...

17/3,K/60 (Item 1 from file: 636)

DIALOG(R)File 636:Gale Group Newsletter DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

04788679 Supplier Number: 65351854 (USE FORMAT 7 FOR FULLTEXT)

IMSYS HEDGES BETS ON JAVA : Rewritable-Microcode Chip Has Instruction Sets for Java, Forth, C/C++.

Halfhill, Tom R.

Microprocessor Report, v14, n8, p36

August, 2000

Language: English Record Type: Fulltext

Document Type: Newsletter; Trade

Word Count: 2648

... implements a garbage collector in microcode, but with a further refinement: the aJ100 can run **multiple JVMs** as independent processes and allow each one to **observe** different rules for **memory** management. One of those JVMs can guarantee real-time response times by disabling the garbage...

17/3,K/61 (Item 2 from file: 636)

DIALOG(R)File 636:Gale Group Newsletter DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

04788678 Supplier Number: 65351853 (USE FORMAT 7 FOR FULLTEXT)

EMBEDDED JAVA CHIPS GET REAL : Bytecode-Native aJ-100 Handles Real-Time Processing.

Halfhill, Tom R.

Microprocessor Report, v14, n8, p31

August, 2000

Language: English Record Type: Fulltext

Document Type: Newsletter; Trade

Word Count: 3192

... with programs that require fast context switching to handle interrupts in real time.

By running **multiple JVMs** as separate processes, the aJ-100 allows software developers to assign different tasks to different JVMs. Moreover, different JVMs can **observe** different rules for **memory** management. The JVM dedicated to real-time processing can disable automatic garbage collection altogether, while other tasks that don't require realtime response can run on **another JVM** that has a normal garbage collector.

Partitioning an application across multiple JVMs is also a...

17/3,K/64 (Item 5 from file: 636)

DIALOG(R)File 636:Gale Group Newsletter DB(TM)

(c) 2004 The Gale Group. All rts. reserv.

SYMANTEC UNVEILS VISUAL JAVA ENTERPRISE SUITE

Computergram International, n3462, pN/A

July 29, 1998

Language: English Record Type: Fulltext

Article Type: Article

Document Type: Newswire; Trade

Word Count: 514

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

...suggests that the new Cafe can do just that. One window in the interface lists **all** the **virtual machines** participating in a given application. Developers can debug each thread in their software, no matter...

...puts it: "Once you have the tools the whole thing becomes more of a Lego **situation** ." **Visual** Cafe for Java Enterprise Suite supports the usual suspects as far as middle-tier application...

?

PLEASE ENTER A COMMAND OR BE LOGGED OFF IN 5 MINUTES

?t17/3,k/11

17/3,K/11 (Item 9 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)

(c) 2004 The Gale Group. All rts. reserv.

01330262 Supplier Number: 41566862

Cabletron Weds Net Management, AI Technology

PC Week, p1

Sept 24, 1990

Language: English Record Type: Abstract

Document Type: Magazine/Journal; Tabloid; General Trade

ABSTRACT:

...based Spectrum network management software system that incorporates artificial intelligence at its core. The software **features** a **graphical** platform for managing and monitoring intergrated multivendor, multiplatform networks. It also has the company's AI-based Inductive Modeling Technology integrated into its **Virtual Network Machine** (VNM) core to allow the software to be more flexible and automatic in manipulating complex...

File 347:JAPIO Nov 1976-2003/Dec(Updated 040402)

(c) 2004 JPO & JAPIO

File 350:Derwent WPIX 1963-2004/UD,UM &UP=200421

(c) 2004 Thomson Derwent

Set	Items	Description
S1	4999	VIRTUAL(1W)MACHINE? ? OR VM OR VMS OR JVM OR JVMs
S2	85	(PLURALIT??? ? OR MULTIPLE OR MANY OR MULTI OR MULTIPLICIT? OR SEVERAL OR NUMEROUS OR MULTITUD? OR PLURIF? OR SECOND OR - BOTH) (1W)S1
S3	91	(VARIOUS OR VARIETY OR GROUP??? ? OR CLUSTER? OR NUMBER OR PAIR???? ? OR SET? ? OR NETWORK? OR CHAIN? ? OR SERIES OR ANO- THER) (1W)S1
S4	18	(DUAL OR THREE OR TWO OR RANGE) (1W)S1
S5	9	ALL(1W)S1
S6	72	(EACH OR INDIVIDUAL? OR SEPARATE?) (1W)S1
S7	2477264	STATUS? OR CONDITION? ? OR MODE OR MODES OR PROPERTY? OR P- ROPERTIES OR PARAMETER? OR PARAMETRE? OR FEATURE OR FEATURES - OR ATTRIBUTE OR ATTRIBUTES
S8	1680802	STATE OR STATES
S9	54142	SITUATION? ? OR CIRCUMSTANCE
S10	1379784	CLASSPATH? OR PATH? ? OR PATHWAY? OR MEMORY OR CLASSES OR - CLASS
S11	131019	S7:S10(3N) (DISPLAY? OR VIEW??? ? OR VISUAL? OR VISUALIS? OR VISUALIZ? OR GRAPHIC???? ? OR ONSCREEN? OR SCREEN? OR VDT? ? OR CRT? ? OR LCD? ? OR VDU? ? OR VIEWSCREEN? OR VIEWPORT?)
S12	83467	S7:S10(3N) (LOOK??? ? OR SEE OR SEES OR SEEING OR SEEN OR S- EEABLE OR SHOW??? ? OR VISIB? OR OBSERVE? ? OR OBSERVAB? OR V- IDEO OR APPEAR?)
S13	1104	S7:S10(3N)PRESENTATION?
S14	3695	S7:S10(3N) (LED OR LEDS)
S15	10	S2:S6 AND S11:S14
S16	10	IDPAT (sorted in duplicate/non-duplicate order)
S17	10	IDPAT (primary/non-duplicate records only)

17/9/2 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2004 Thomson Derwent. All rts. reserv.

014806373 **Image available**

WPI Acc No: 2002-627079/200267

XRPX Acc No: N02-495986

**Computer-implemented builder tool for multiple virtual machine
system, has parameter detail component which displays parameter
information associated with one of virtual machines of overview component**
Patent Assignee: FRERKING M J (FRER-I); HARDIN D S (HARD-I); MYKRIS N M
(MYKR-I); WILEY P J (WILE-I)

Inventor: FRERKING M J; HARDIN D S; MYKRIS N M; WILEY P J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020095396	A1	20020718	US 2001261925	A	20010116	200267 B
			US 2001681263	A	20010309	

Priority Applications (No Type Date): US 2001261925 P 20010116; US
2001681263 A 20010309

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20020095396	A1		41	G06F-007/00	Provisional application US 2001261925

Abstract (Basic): US 20020095396 A1

NOVELTY - An over view components presented in a display component depicts **multiple virtual machines** . A **parameter** detail component **displays parameter** information associated with one of the virtual machines of the overview component.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

(1) Target executable file creation method; and
(2) Computer readable storage medium storing target executable file creation program.

USE - For creating target executable file for **multiple virtual machine** environment, embedded environment and also for use with JAVA virtual machines, etc.

ADVANTAGE - Provides user friendly **multiple virtual machine** builder environment. Determines both the build information and various project and virtual machine parameter efficiently for **multiple virtual machine** environment. Efficiently manages allocations of resources among **various virtual machines** .

DESCRIPTION OF DRAWING(S) - The figure shows the various inputs to the builder tool for **multiple virtual machine** environment and resultant target executable file.

pp; 41 DwgNo 1/31

Title Terms: COMPUTER; IMPLEMENT; BUILD; TOOL; MULTIPLE; VIRTUAL; MACHINE; SYSTEM; PARAMETER; DETAIL; COMPONENT; DISPLAY; PARAMETER; INFORMATION; ASSOCIATE; ONE; VIRTUAL; MACHINE; COMPONENT

Derwent Class: T01

International Patent Class (Main): G06F-007/00

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05G3; T01-F07; T01-J20B; T01-S03

17/9/4 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2004 Thomson Derwent. All rts. reserv.

011164368 **Image available**

WPI Acc No: 1997-142293/199713

XRFX Acc No: N97-117814

VM system - has switching processing part to switch attachment place of console to VM based on switching command output from console

Patent Assignee: FUJITSU LTD (FUJIT)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 9022365	A	19970121	JP 95169598	A	19950705	199713 B

Priority Applications (No Type Date): JP 95169598 A 19950705

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 9022365	A	10	G06F-009/46	

Abstract (Basic): JP 9022365 A

The system has a controlling mechanism (2) to control **number** of **VMs** (1) which are grouped. A console (3) operates the VMs. A definition part (20) of the controlling mechanism manages the state information of VMs belonging to **each VM** group. A **state display** part (21) outputs the state information of **all VMs** to the console. When the console publishes a command indicating the operation of the VM group, it is published to **all VMs** of the VM group.

A package operating part (22) outputs the execution result of operation of VM to the console. Based on the switching command output

from the console, a switching processing part (23) switches the attachment place of the console to the corresponding VM.

ADVANTAGE - Reduces operators load. Enables operator to operate by checking state of VM. Inhibits incorrect operation and generation of idle state. Shares VM and OS console.

Dwg.1/14

Title Terms: SYSTEM; SWITCH; PROCESS; PART; SWITCH; ATTACH; PLACE; CONSOLE;
BASED; SWITCH; COMMAND; OUTPUT; CONSOLE
Index Terms/Additional Words: VIRTUAL; MACHINE
Derwent Class: T01
International Patent Class (Main): G06F-009/46
File Segment: EPI
Manual Codes (EPI/S-X): T01-F02; T01-F05G3

17/9/5 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2004 Thomson Derwent. All rts. reserv.

007998313 **Image available**

WPI Acc No: 1989-263425/198936

Related WPI Acc No: 1996-058105

XRPX Acc No: N89-200999

Virtual machine programming system for computer - permits operator to program computer by directing synthesis of hierarchy of virtual machines

Patent Assignee: FLUKE J MFG (FLUK-N)

Inventor: BHASKAR K S; PECKOL J K

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 4849880	A	19890718	US 85798931	A	19851118	198936 B

Priority Applications (No Type Date): US 85798931 A 19851118

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 4849880	A	55		

Abstract (Basic): US 4849880 A

The system for programming a computer provides a set of software-based virtual machines each for instructing a computer to carry out a selected operation. Each virtual machine is represented by a virtual front panel displayed on a screen and each virtual front panel graphically displays operator controllable values of input and output parameters utilised by the virtual machine it represents.

The system is adapted to synthesise a New virtual machine for instructing the computer to perform a sequence of operations wherein each operation is carried out by the computer according to the instructions of an operator selected one of the existing virtual machines. The system also creates a new virtual front panel for **displaying** input and output **parameters** associated with the new virtual machine. The system permits the operator to program the computer by directing synthesis of a hierarchy of virtual machines.

USE - For organising computer operations, for developing and structuring computer applications software for programming computer -based instrument system.

Dwg.1/8

Title Terms: VIRTUAL; MACHINE; PROGRAM; SYSTEM; COMPUTER; PERMIT; OPERATE;
PROGRAM; COMPUTER; DIRECT; SYNTHESIS; HIERARCHY; VIRTUAL; MACHINE
Derwent Class: S01; T01
International Patent Class (Additional): G06F-015/00

File Segment: EPI
Manual Codes (EPI/S-X): S01-H; T01-H02; T01-J08; T01-J20

17/9/8 (Item 8 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2004 JPO & JAPIO. All rts. reserv.

04001938 **Image available**
MULTI-OS OPERATING METHOD

PUB. NO.: 04-367038 [JP 4367038 A]
PUBLISHED: December 18, 1992 (19921218)
INVENTOR(s): IKEGAYA NAOKO
TANAKA TOSHIHARU
UMENO HIDENORI
OHARA NOBORU
HIROTAKA HIROSHI
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP
(Japan)
APPL. NO.: 03-143011 [JP 91143011]
FILED: June 14, 1991 (19910614)
INTL CLASS: [5] G06F-009/46; G06F-003/14
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units);
45.3 (INFORMATION PROCESSING -- Input Output Units)
JOURNAL: Section: P, Section No. 1536, Vol. 17, No. 246, Pg. 124, May
17, 1993 (19930517)

ABSTRACT

PURPOSE: To facilitate the operation of plural OSs used in a virtual computer system by starting and controlling the OSs through a single terminal equipment, and also to improve the utilizing efficiency of the terminal equipment by **showing** the executing **states** of those OSs on a single terminal equipment.

CONSTITUTION: If a VM generating command is inputted from a certain terminal equipment, the attribute of the new VM is registered into a **multi - VM0** control list (303). The subject plural VMs are decided (304) with a VM control command which designates plural VMs. Then the execution of VM control commands are requested to a VMCP for **all VMs** (308). An OS display buffer and the OS display priority order are registered (306, 307) into the **multi - VM** control list with a start command of plural operated OSs. Then **each VM** is started (308).

17/9/9 (Item 9 from file: 347)
DIALOG(R)File 347:JAPIO
(c) 2004 JPO & JAPIO. All rts. reserv.

02539349 **Image available**
MACHINE CHECK AND CONTROL SYSTEM FOR VIRTUAL COMPUTER

PUB. NO.: 63-156249 [JP 63156249 A]
PUBLISHED: June 29, 1988 (19880629)
INVENTOR(s): KATO MOTOKAZU
APPLICANT(s): FUJITSU LTD [000522] (A Japanese Company or Corporation), JP
(Japan)
APPL. NO.: 61-304507 [JP 86304507]
FILED: December 19, 1986 (19861219)
INTL CLASS: [4] G06F-009/46; G06F-011/30
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units)

JOURNAL: Section: P, Section No. 783, Vol. 12, No. 422, Pg. 40,
November 09, 1988 (19881109)

ABSTRACT

PURPOSE: To perform operation without any contradiction by stopping a computer if machine check occurs during running of a SM program when a virtual machine (VM) program inhibits a machine check interruption but the SM program supporting the VM program permits said interruption.

CONSTITUTION: A state set input from a control circuit **sets** a **VM** or SM program running state 11 or 12 to a **state display** means 1. An interruption permission set input sets machine check interruption permitting means V2 and S3 in VM and SM program running states. If machine check 6 occurs when the means V2 is set to an interruption inhibiting state 12 and the means S3 is set to an interruption permitting state 14, a condition setting means 4 issues a stop instruction 7 to the control circuit of the computer and displays this issue on a specific **state display** means 5. Thus, interruption permission settings to respective machine check are operated without any contradiction.

File 348:EUROPEAN PATENTS 1978-2004/Apr W01

(c) 2004 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20040408,UT=20040401

(c) 2004 WIPO/Univentio

Set	Items	Description
S1	17986	VIRTUAL(1W)MACHINE? ? OR VM OR VMS OR JVM OR JVMs
S2	316	(PLURALIT??? ? OR MULTIPLE OR MANY OR MULTI OR MULTIPLICIT? OR SEVERAL OR NUMEROUS OR MULTITUD? OR PLURIF? OR SECOND OR - BOTH) (1W)S1
S3	490	(VARIOUS OR VARIETY OR GROUP??? ? OR CLUSTER? OR NUMBER OR PAIR???? ? OR SET? ? OR NETWORK? OR CHAIN? ? OR SERIES OR ANO- THER) (1W)S1
S4	138	(DUAL OR THREE OR TWO OR RANGE) (1W)S1
S5	86	ALL(1W)S1
S6	263	(EACH OR INDIVIDUAL? OR SEPARATE?) (1W)S1
S7	1259230	STATUS? OR CONDITION? ? OR MODE OR MODES OR PROPERTY? OR P- ROPERTIES OR PARAMETER? OR PARAMETRE? OR FEATURE OR FEATURES - OR ATTRIBUTE OR ATTRIBUTES
S8	1063972	STATE OR STATES
S9	241270	SITUATION? ? OR CIRCUMSTANCE
S10	603539	CLASSPATH? OR PATH? ? OR PATHWAY? OR MEMORY OR CLASSES OR - CLASS
S11	115612	S7:S10(3N) (DISPLAY? OR VIEW??? ? OR VISUAL? OR VISUALIS? OR VISUALIZ? OR GRAPHIC???? ? OR ONSCREEN? OR SCREEN? OR VDT? ? OR CRT? ? OR LCD? ? OR VDU? ? OR VIEWSCREEN? OR VIEWPORT?)
S12	288100	S7:S10(3N) (LOOK??? ? OR SEE OR SEES OR SEEING OR SEEN OR S- EEABLE OR SHOW??? ? OR VISIB? OR OBSERVE? ? OR OBSERVAB? OR V- IDEO OR APPEAR?)
S13	4170	S7:S10(3N)PRESENTATION?
S14	6210	S7:S10(3N) (LED OR LEDS)
S15	38	S2:S6(25N)S11:S14
S16	38	IDPAT (sorted in duplicate/non-duplicate order)
S17	37	IDPAT (primary/non-duplicate records only)

17/5,K/2 (Item 2 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01637468

Optimization of memory usage based on garbage collection simulation

Optimierung der Speicherverwendung auf Grundlage der Simulation der
Freispeichersammlung

Optimisation d'usage de la memoire basee sur la simulation du
ramasse-miettes memoire

PATENT ASSIGNEE:

Hewlett-Packard Company, (206037), 3000 Hanover Street, Palo Alto, CA
94304, (US), (Applicant designated States: all)

INVENTOR:

Coha, Joseph A., 1462 Bing Drive, San Jose. CA 95129, (US)

Karkare, Ashish, 1272 Harefield Drive, San Jose, CA 95131, (US)

O'Konski, Timothy C., 222 Byron Street, Palo Alto, CA 94301, (US)

LEGAL REPRESENTATIVE:

Jehan, Robert et al (72665), Williams Powell, Morley House, 26-30 Holborn
Viaduct, London EC1A 2BP, (GB)

PATENT (CC, No, Kind, Date): EP 1349077 A2 031001 (Basic)

APPLICATION (CC, No, Date): EP 2003250706 030204;

PRIORITY (CC, No, Date): US 104751 020321

DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR;
HU; IE; IT; LI; LU; MC; NL; PT; SE; SI; SK; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO

INTERNATIONAL PATENT CLASS: G06F-012/02; G06F-011/34

ABSTRACT EP 1349077 A2

A method for optimization of memory usage for a computer application. Memory usage data (720) is received (215) wherein the memory usage data (720) comprises timing information. A graphical representation (740) of the memory usage data (720) is generated (225). At least one heap parameter (725) is received (230). A memory usage simulation is performed (245) based on the memory usage data (720) and the heap parameter (725).

ABSTRACT WORD COUNT: 69

NOTE:

Figure number on first page: 7

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 031001 A2 Published application without search report
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200340	305
SPEC A	(English)	200340	4492
Total word count - document A			4797
Total word count - document B			0
Total word count - documents A + B			4797

...SPECIFICATION or uncheck the box depending on the nature of the application.

In one embodiment, heap **parameter** input **view** 505 comprises a JVM selector 530. In one embodiment, the JVM selector 530 is a pull-down menu comprising a **plurality** of **JVM** versions. JVM selector initially illustrates which JVM version/type was used in the application run...

17/5,K/5 (Item 5 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01079106

High speed virtual machine and compiler

Hochgeschwindige virtuelle Maschine und Compiler

Machine virtuelle a grande vitesse et compilateur

PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., (216883), 1006, Oaza Kadoma, Kadoma-shi, Osaka-fu, 571, (JP), (Applicant designated States: all)

INVENTOR:

Waki, Hiroyuki, 3-17-30-102, Makinosaka, Hirakata-shi, Osaka-fu 573-1146, (JP)

Inoue, Shinji, 19-1-1142, Matsuya-cho, Neyagawa-shi, Osaka-fu 572-0086, (JP)

Hayama, Satoru, 2-12, Sumiyoshidai, Higashinada-ku, Kobe-shi, Hyogo-ken 658-0062, (JP)

Fujita, Mitsuko, 3-5-20, Nakameguro, Meguro-ku, Tokyo-to 153-0061, (JP)

Ishikawa, Akira, 226-3, Kitsunai, Kashiba-shi, Nara-ken 639-0234, (JP)

LEGAL REPRESENTATIVE:

Crawford, Andrew Birkby et al (29761), A.A. THORNTON & CO. Northumberland House 303-306 High Holborn, London WC1V 7LE, (GB)

PATENT (CC, No, Kind, Date): EP 949564 A2 991013 (Basic)

APPLICATION (CC, No, Date): EP 99302715 990407;

PRIORITY (CC, No, Date): JP 9896204 980408

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-009/00

ABSTRACT EP 949564 A2

A virtual machine with a stack architecture includes: a stack 120 whose top level (TOS) and the second level from the top (SOS) are mapped to registers of a real machine 201; an instruction storing unit 102 for storing a virtual machine instruction sequence to be executed; next instruction information storing unit 101 for storing a plurality of sets of next instruction information that are each associated with a different virtual machine instruction in the virtual machine instruction sequence, the set of next instruction information for a given virtual machine instruction indicating a change in a number of sets of data stored in the stack 120 due to execution of a virtual machine instruction executed after the given virtual machine instruction; a decoding unit 103 for decoding a virtual machine instruction and an associated set of next instruction information after reading them from the instruction storing unit 102 and the next instruction information storing unit 101; and an executing unit 110 for executing the decoded virtual machine instruction and performing a stack handling in the stack 120 in advance for a virtual machine instruction that is to be executed next based on the set of next instruction information.

ABSTRACT WORD COUNT: 199

NOTE:

Figure number on first page: NONE

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 991013 A2 Published application without search report
Change: 991020 A2 International Patent Classification changed:
19990827

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9941	5065
SPEC A	(English)	9941	30403
Total word count - document A			35468
Total word count - document B			0
Total word count - documents A + B			35468

...SPECIFICATION virtual machine instructions as decoded data to the executing unit 4410.

Figs. 11A and 11B show the states of the PC 4404, the SP 4412, and the stack 4420 before and after the execution of the each virtual machine instruction when the executing unit 4410 executes the virtual machine program shown in Fig. 10A...data transfer between the registers and the memory 202 is necessary.

Fig. 42 is a state transition diagram showing changes in virtual machine instruction types. Here, each state in the state transition corresponds to an instruction type for each virtual machine instruction to be executed by the virtual machine 100 of the present embodiment. These instruction...

17/5,K/11 (Item 11 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2004 European Patent Office. All rts. reserv.

00669696

Method for building a sub-network in a distributed voice messaging system
Verfahren zum Aufbau eines Unternetzes in einem verteilten
Sprachnachrichtensystem

Methode pour realiser un sous-reseau dans un systeme de messagerie vocale distribue

PATENT ASSIGNEE:

Siemens Information and Communication Networks, Inc., (2620744), 4900 Old Ironside Drive, Santa Clara, CA 95054, (US), (Proprietor designated states: all)

INVENTOR:

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Preysman, Irene, 1417 Harrier Court, Sunnyvale, CA 94087, (US)

Jain, Narinder, 945 Stonehurst Way, Campbell, CA 95008, (US)

LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 643521 A2 950315 (Basic)

EP 643521 A3 991103

EP 643521 B1 030625

APPLICATION (CC, No, Date): EP 94114156 940908;

PRIORITY (CC, No, Date): US 120985 930914

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL

INTERNATIONAL PATENT CLASS: H04M-003/42; H04M-003/50; H04L-012/28

CITED PATENTS (EP B): US 5018133 A; US 5130980 A

CITED REFERENCES (EP B):

PATENT ABSTRACTS OF JAPAN vol. 014, no. 546 (E-1008), 4 December 1990

(1990-12-04) & JP 02 231843 A (FUJITSU LTD), 13 September 1990

(1990-09-13)

PATENT ABSTRACTS OF JAPAN vol. 013, no. 584 (E-866), 22 December 1989

(1989-12-22) & JP 01 245633 A (NEC CORP), 29 September 1989

(1989-09-29);

ABSTRACT EP 643521 A2

Method for building a sub-network for a distributed Voice Messaging System that can be initiated from any of the nodes in the sub-network. For the distributed application to work, all the nodes in the network being created must be identified, numbered, and a map of the network distributed to all Voice Messaging System nodes. Advantageously, the inventive method enables logically independent sub-networks of Voice Messaging Systems to co-exist in the same physical LAN. An embodiment of the present invention is a method for building a sub-network of nodes in a communication network, the method including the steps of: (a) identifying nodes to be included in the sub-network; (b) gathering node configuration information from the nodes identified; (c) resolving node configuration information conflicts among the nodes in the sub-network; (d) create a sub-network map; (e) distributing the sub-network map to the nodes in the sub-network; and (f) synchronizing the nodes in the sub-network. (see image in original document)

ABSTRACT WORD COUNT: 159

NOTE:

Figure number on first page: 5

LEGAL STATUS (Type, Pub Date, Kind, Text):

Assignee: 000621 A2 Transfer of rights to new applicant: Siemens Information and Communication Networks, Inc. (2620744) 4900 Old Ironside Drive Santa Clara, CA 95054 US

Examination: 20000202 A2 Date of request for examination: 19991206

Lapse: 040324 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 20030625, CH 20030625, LI 20030625, ES 20031222, NL 20030625,

Lapse: 040114 B1 Date of lapse of European Patent in a

contracting state (Country, date): AT
20030625, CH 20030625, LI 20030625,

Grant: 030625 B1 Granted patent

Change: 020327 A2 Legal representative(s) changed 20020206

Examination: 020327 A2 Date of dispatch of the first examination
report: 20020211

Change: 000621 A2 Legal representative(s) changed 20000428

Change: 030129 A2 Title of invention (German) changed: 20021209

Change: 030129 A2 Title of invention (English) changed: 20021209

Lapse: 040102 B1 Date of lapse of European Patent in a
contracting state (Country, date): CH
20030625, LI 20030625,

Lapse: 040303 B1 Date of lapse of European Patent in a
contracting state (Country, date): AT
20030625, CH 20030625, LI 20030625, NL
20030625,

Application: 950315 A2 Published application (A1with Search Report
;A2without Search Report)

*Assignee: 950607 A2 Applicant (transfer of rights) (change):
SIEMENS ROLM COMMUNICATIONS INC. (1575232) 4900
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*Assignee: 950607 A2 Previous applicant in case of transfer of
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AT;BE;CH;DE;ES;FR;GB;IT;LI;NL)

*Assignee: 970423 A2 Applicant (transfer of rights) (change):
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AT;BE;CH;DE;ES;FR;GB;IT;LI;NL)

*Assignee: 970423 A2 Previous applicant in case of transfer of
rights (change): SIEMENS ROLM COMMUNICATIONS
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states: AT;BE;CH;DE;ES;FR;GB;IT;LI;NL)

Change: 980708 A2 Representative (change)

Search Report: 991103 A3 Separate publication of the search report

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB95	220
CLAIMS B	(English)	200326	343
CLAIMS B	(German)	200326	321
CLAIMS B	(French)	200326	378
SPEC A	(English)	EPAB95	3651
SPEC B	(English)	200326	5497
Total word count - document A			3872
Total word count - document B			6539
Total word count - documents A + B			10411

...SPECIFICATION or confirmation. The Config. Util. initiates and sequences the building process. It makes sure that **all** the **VM** nodes requested by the user are included in the sub-network and it **displays status** information related to the building process.

(b) Node Supervisor (NS) Layer contains software that offers...

...SPECIFICATION or confirmation. The Config. Util. initiates and sequences

the building process. It makes sure that all the VM nodes requested by the user are included in the sub-network and it displays status information related to the building process.

(b) Node Supervisor (NS) Layer contains software that offers...

17/5,K/12 (Item 12 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00508961

NETWORK MANAGEMENT SYSTEM USING MODEL-BASED INTELLIGENCE
NETZWERKVERWALTUNGSSYSTEM MIT MODELLBASIERTER INTELLIGENZ
SYSTEME DE GESTION DE RESEAU UTILISANT UNE INTELLIGENCE BASEE SUR DES
MODELES

PATENT ASSIGNEE:

CABLETRON SYSTEMS, INC., (1353621), 35 Industrial Way, Rochester, NH
03867, (US), (applicant designated states:
AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;LU;NL;SE)

INVENTOR:

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RUSTICI, Eric, S., 1 Wyandot Circle, Londonderry, NH 03053, (US)
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WIGGIN, Dwayne, S., 11 Country Ridge Park, Rochester, NH 03867, (US)
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SCOTT, Walter, P., 6 Linsing Drive, Salem, NH 03079, (US)

LEGAL REPRESENTATIVE:

Read, Matthew Charles et al (47911), Venner Shipley & Co. 20 Little
Britain, London EC1A 7DH, (GB)

PATENT (CC, No, Kind, Date): EP 549677 A1 930707 (Basic)
EP 549677 B1 970625
WO 9205485 920402

APPLICATION (CC, No, Date): EP 91916905 910917; WO 91US6725 910917

PRIORITY (CC, No, Date): US 583509 900917

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G06F-009/46; G06F-015/16;

CITED REFERENCES (WO A):

Proceedings of the Ninth Data Communications Symposium, 10-13 September
1985, Whistler Mountain, British Columbia, R.S. Gilbert al.: "CNMGRAF -
Graphic presentation services for network management", pages 199-206,
see page 199, column 2, part 2 - page 200, column 1, line 8; page 200,
column 1, part 3 - column 2, line 12; page 200, figure 1; page 201,
column 1, lines 30-50; page 204, column 1, lines 1-27; page 204, column
2, lines 7-13

Electronic Design, volume 37, no. 21, 12 October 1989, (Cleveland, US) D.
Bursky: "Simulator eases communication network design", pages
97-98,100, see the whole article;

NOTE:

No A-document published by EPO

LEGAL STATUS (Type, Pub Date, Kind, Text):

Lapse: 020619 B1 Date of lapse of European Patent in a
contracting state (Country, date): AT
19970625, BE 19970625, CH 19970625, LI
19970625, DK 19970625, ES 19970625, GR
19970625, IT 19970625, LU 19970930,

Lapse: 20000202 B1 Date of lapse of European Patent in a
contracting state (Country, date): AT
19970625, BE 19970625, CH 19970625, LI
19970625, DK 19970625, GR 19970625, IT
19970625, LU 19970930,

Application: 930707 A1 Published application (A1with Search Report
 ;A2without Search Report)
 Lapse: 20000209 B1 Date of lapse of European Patent in a
 contracting state (Country, date): AT
 19970625, BE 19970625, CH 19970625, LI
 19970625, DK 19970625, GR 19970625, IT
 19970625, LU 19970930,
 Examination: 930707 A1 Date of filing of request for examination:
 930401
 *Examination: 930721 A1 Date of filing of request for examination
 (change): 930326
 Examination: 931027 A1 Date of despatch of first examination report:
 930909
 Change: 970618 A1 Miscellaneous (change)
 Grant: 970625 B1 Granted patent
 Lapse: 980107 B1 Date of lapse of the European patent in a
 Contracting State: AT 970625
 Lapse: 980121 B1 Date of lapse of the European patent in a
 Contracting State: AT 970625, CH 970625, LI
 970625
 Lapse: 980121 B1 Date of lapse of the European patent in a
 Contracting State: AT 970625, CH 970625, LI
 970625
 Lapse: 980304 B1 Date of lapse of the European patent in a
 Contracting State: AT 970625, BE 970625, CH
 970625, LI 970625
 Lapse: 980408 B1 Date of lapse of the European patent in a
 Contracting State: AT 970625, BE 970625, CH
 970625, LI 970625, DK 970625
 Oppn None: 980617 B1 No opposition filed
 Lapse: 991020 B1 Date of lapse of European Patent in a
 contracting state (Country, date): AT
 19970625, BE 19970625, CH 19970625, LI
 19970625, DK 19970625, IT 19970625,

LANGUAGE (Publication,Procedural,Application): English; English; English
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB97	529
CLAIMS B	(German)	EPAB97	513
CLAIMS B	(French)	EPAB97	541
SPEC B	(English)	EPAB97	7657
Total word count - document A			0
Total word count - document B			9240
Total word count - documents A + B			9240

...SPECIFICATION contains a description of the defined attribute. When the
 user asks for help regarding this **attribute** , the text string **appears**
 on the user interface **screen** .

6. **Attribute** value is the value of the attribute.

The models used in the **virtual network machine** also include one
 or more inference handlers. An inference handler is a C++ object which...
 user clicks on an icon to proceed to another view, the icon manager
 determines the **view class** and the next **view** . The icon manager then
 issues a new view by passing the **view class** and the appropriate
virtual network machine model ID to the view executive, thereby
 causing the current view to be destroyed.

The...

?

PLEASE ENTER A COMMAND OR BE LOGGED OFF IN 5 MINUTES

? t17/5, k/13, 21, 25, 27, 37

17/5,K/13 (Item 13 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2004 European Patent Office. All rts. reserv.

00469012

Multiple controlled data-space facility.

System mit mehreren gesteuerten Datenraumen.

Systeme a plusieurs espaces de donnees commandes.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Brelsford, David Paul, 01 Pine Tree Lane, Hyde Park, New York 12538, (US)
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Lafitte, Jean Louis, Route des Alpes, F-01280 Moens, (FR)

Gdaniec, Joseph Martin, 20 Crumwold Place, Hyde Park, New York 12538,
(US)

Osisek, Damian Leo, 420 Jodie Drive, Vestal, New York 13850-3257, (US)
Plambeck, Kenneth Ernest, 7 Daisy Lane, Poughkeepsie, New York 12603,
(US)

LEGAL REPRESENTATIVE:

Jost, Ottokarl, Dipl.-Ing. (6092), IBM Deutschland GmbH Patentwesen und
Urheberrecht Schonaicher Strasse 220, W-7030 Boblingen, (DE)

PATENT (CC, No, Kind, Date): EP 478978 A2 920408 (Basic)
EP 478978 A3 930324

APPLICATION (CC, No, Date): EP 91115014 910905;

PRIORITY (CC, No, Date): US 577395 901002

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-009/46;

CITED PATENTS (EP A): US 4695950 A; US 4945480 A

CITED REFERENCES (EP A):

IBM TECHNICAL DISCLOSURE BULLETIN vol. 33, no. 3B, August 1990, NEW YORK
US page 10 'Inter-Virtual Machine Resource Locking Mechanism'

12TH ANNUAL INTERNATIONAL SYMPOSIUM ON COMPUTER ARCHITECTURE 17 June
1985, BOSTON, MA pages 171 - 178 S.NANBA ET AL. 'VM/4:ACOS-4 Virtual
Machine Architecture'

IBM SYSTEMS JOURNAL vol. 28, no. 1, 1989, ARMONK, NEW YORK US pages 39 -
61 K.E.PLAMBECK 'Concepts of Enterprise Systems Architecture /370'

MICROPROCESSING AND MICROPROGRAMMING vol. 22, no. 2, February 1988,
AMSTERDAM NL pages 81 - 96 S.R.TSAI ET AL. 'On the Architectural
Support for Logical Machine Systems';

ABSTRACT EP 478978 A2

A method and apparatus for providing common access to data spaces by a
plurality of virtual machine guests emulated on a host computer system. A
token (802) received from a given one of the virtual machine guests is
used to identify a particular host data space. The guest also supplies an
offset (803). The offset (803) and the identified host data space are
then used to derive a host absolute address representative (801) of a
data location in the host data space. (see image in original document)

ABSTRACT WORD COUNT: 89

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 920408 A2 Published application (Alwith Search Report
;A2without Search Report)

Examination: 921014 A2 Date of filing of request for examination:
920817

Search Report: 930324 A3 Separate publication of the European or
International search report

Withdrawal: 961113 A2 Date on which the European patent application

was withdrawn: 960923

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	910
SPEC A	(English)	EPABF1	9783
Total word count - document A			10693
Total word count - document B			0
Total word count - documents A + B			10693

...SPECIFICATION register-specified address spaces.

It is still a further object of the invention to provide **individual virtual machines**, running without guest dynamic address translation and guest access register translation, with the ability to **view** system **memory** either disparately or in the same manner depending on the desired operating environment.

In accord...

✓ 17/5,K/21 (Item 21 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00872842 **Image available**

SYSTEM AND METHOD FOR DISPLAYING CURRENT IMAGES OF VIRTUAL MACHINE ENVIRONMENTS

SYSTEME ET PROCEDE POUR AFFICHER LES IMAGES COURANTES D'ENVIRONNEMENTS DE MACHINE VIRTUELLE

Patent Applicant/Assignee:

CONNECTIX CORPORATION, 2955 Campus Drive, Suite 100, San Mateo, CA 94403,
US, US (Residence), US (Nationality)

Inventor(s):

TRAUT Eric P, 3 Iris Lane, San Carlos, CA 94070, US,
MARTZ Benjamin, 258 W. 40th Ave., San Mateo, CA 94403, US,

Legal Representative:

FULGHUM Roger (agent), Baker Botts L.L.P., One Shell Plaza, 910
Louisiana, Houston, TX 77002, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200206941 A2-A3 20020124 (WO 0206941)

Application: WO 2001US22278 20010716 (PCT/WO US0122278)

Priority Application: US 2000617669 20000717

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU

CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP

KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD

SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/455

International Patent Class: G06F-009/44; G06F-003/033

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 5528

English Abstract

A system and method for displaying thumbnail images of the video output

of one or more software applications in a window or similar graphical interface to allow the user of a computer system to conveniently and quickly monitor the overall status and progress of several software applications that are running simultaneously. The thumbnail images are generated from the VRAM associated with the software application and are preferably displayed with information corresponding to the associated software application. The thumbnail images may be static or generated at regular intervals according to user preference and the status of the software application.

French Abstract

Système et procédé pour afficher des imagerie de la sortie vidéo d'une ou plusieurs applications logicielles dans une fenêtre ou une interface graphique similaire pour permettre à l'utilisateur d'un système informatique de surveiller aisément et rapidement le statut général et l'évolution de plusieurs applications logicielles qui tournent simultanément. Les imagerie sont générées à partir de la RAM associée à l'application logicielle et sont de préférence affichées dans l'application logicielle associée. Les imagerie peuvent être statiques ou générées à des intervalles réguliers selon les préférences de l'utilisateur et le statut de l'application logicielle.

Legal Status (Type, Date, Text)

Publication	20020124	A2 Without international search report and to be republished upon receipt of that report.
Examination	20020516	Request for preliminary examination prior to end of 19th month from priority date
Search Rpt	20031218	Late publication of international search report
Republication	20031218	A3 With international search report.
Republication	20031218	A3 Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

Fulltext Availability:

Detailed Description
Claims

Detailed Description

... present invention is that it allows a user of a computer system that is running **multiple virtual machine** environments to **view the status of several multiple virtual machine** environments at one glance. This allows the user to easily manage **several virtual machine** environments that are running simultaneously. Another advantage of the present system and method is that...any of the virtual machines or emulators, among other examples.

One or more virtual machine **status** windows are **displayed** within virtual machine list window 14. Shown in list window 14 are virtual machine status windows 20a-20i. Each virtual machine status window is preferably associated with one of the virtual machines running on computer system...

...software application, then a virtual machine status window can be associated with each software application. **Each virtual machine status window displays** information regarding the associated virtual machine or emulator. For example, the virtual machine **status** window may **display** information regarding the operating system or computer system that is being emulated, the software application...

Claim

... wherein the reduced-size representations are generated from the video information stored in the virtual video memory associated with each virtual machine .
. A method for displaying a reduced-size image of multiple emulated computer systems, comprising the...

✓ 17/5,K/25 (Item 25 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00783154 **Image available**

**COMPUTERIZED MACHINE CONTROLLER DIAGNOSTIC SYSTEM
SYSTEME DE DIAGNOSTIC DE COMMANDE DE MACHINE INFORMATISEE**

Patent Applicant/Assignee:

AUTOMATED DIAGNOSTIC SYSTEMS LLC, 75 West Viking Drive, Suite 101, Little Canada, MN 55117, US, US (Residence), US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

WILSON Dennis, 75 West Viking Drive, Suite 101, Little Canada, MN 55117, US, US (Residence), US (Nationality)
DEVILEY Brian, 4346 Jennifer Court, Eagan, MN 55123, US, US (Residence), US (Nationality)

Legal Representative:

BAXTER Kimberly K (et al) (agent), Patterson, Thunte, Skaar & Christensen, P.A, 4800 IDS Center, 80 South 8th Street, Minneapolis, MN 55402-2100, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116660 A2-A3 20010308 (WO 0116660)
Application: WO 2000US23765 20000830 (PCT/WO US0023765)
Priority Application: US 99151499 19990830;~~US-2000180936 20000208;~~ US 2000189945 20000316

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G05B-023/02

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 16331

English Abstract

A computerized machine control (CMC) diagnostic system communicates with a computerized machine controller (12) that utilizes a control program to control the operation of a machine through the use of plurality of digital channels. In general terms, the system includes a data acquisition component (14), a data storage component (16) and a viewing component (18). The data acquisition component queries and acquires time-based transition data about the plurality of digital channels. The data storage component stores the acquired transition data in order to establish an historical pattern of the transition data that can be compared to currently acquired transition data. The historical pattern of transition data is established independently of the control program. Upon

comparison of the current transition data to the expected, historical pattern of transition data, a determination about the operational status of the machine being controlled can be made. The operational status can then be displayed on the viewing component.

French Abstract

L'invention concerne un systeme de diagnostic de commande de machine informatisee (CMC), qui communique avec une commande de machine informatisee utilisant un programme de commande pour commander le fonctionnement de la machine par l'utilisation de plusieurs canaux numeriques. Le systeme comporte generalement un composant d'acquisition de donnees, un composant de stockage de donnees et un composant de visualisation. Le composant d'acquisition de donnees demande et acquiert des donnees de transition temporelles relatives aux canaux numeriques. Le composant de stockage de donnees stocke les donnees de transition acquises de facon a etabli un schema historique des donnees de transition pouvant se comparer a des donnees de transition acquises actuellement. Le schema historique des donnees de transition est etabli independamment du programme de commande. Lors de la comparaison des donnees de transition actuelles et du schema historique attendu des donnees de transition, il est possible de determiner le statut operationnel de la machine commandee. Le statut operationnel peut alors etre affiche sur le composant de visualisation.

Legal Status (Type, Date, Text)

Publication 20010308 A2 Without international search report and to be republished upon receipt of that report.
Examination 20010531 Request for preliminary examination prior to end of 19th month from priority date
Search Rpt 20011011 Late publication of international search report
Republication 20011011 A3 With international search report.
Search Rpt 20011011 Late publication of international search report
Correction 20020912 Corrected version of Pamphlet: pages 1-48, description, replaced by new pages 1-42; pages 49-60, claims, replaced by new pages 43-52; pages 1/15-15/15, drawings replaced by new pages 1/14-14/14; due to late transmittal by the receiving Office
Republication 20020912 A3 With international search report.

Fulltext Availability: Claims

Claim

... of claim 48, wherein said machine comprise a plurality of machines, and said viewing component **display** the operational **status** of said plurality of machines substantially simultaneously.

50 The system of claim 49, wherein said plurality of machines comprise a **plurality** of **virtual machines** .

51 The system of claim 49, wherein said plurality of machines performs different functions.

52...

00739208 **Image available**

A COMPUTER IMPLEMENTED METHOD

PROCEDE MIS EN OEUVRE PAR ORDINATEUR

Patent Applicant/Assignee:

SONY ELECTRONICS INC, 1 Sony Drive, Park Ridge, NJ 07656, US, US
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Inventor(s):

LI Qiaoyun, 19400 Sorenson Avenue #101, Cupertino, CA 95014, US

Legal Representative:

GALLENSON Mavis S, Ladas & Parry, Suite 2100, 5670 Wilshire Boulevard,
Los Angeles, CA 90036-5679, US

Patent and Priority Information (Country, Number, Date):

Patent: WO 200052572 A1 20000908 (WO 0052572)

Application: WO 99US28859 19991206 (PCT/WO US9928859)

Priority Application: US 99260259 19990301

Designated States: AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK

DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ

TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/445

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 9894

English Abstract

A computer-implemented method and system for allowing Java classes to be shared among many Java virtual machines (JVMs) including a communication system allowing Java and native applications to readily interoperate. An implementation of the JVM on an operating system platform, e.g., the AperiOS AV/OS, allows a variety of applications including desktop applications, applets and Internet based applications, home networking applications, MHEG-6 applets, gaming, gaming applications and next generation audio visual applications to operate with the operating system. The present invention provides a shared memory pool (SMP) into which a JVM and store and register a particular Java class. The stored and registered Java class is then accessible by other JVMs using the SMP and a Java layer class manager that is implemented in software. The Java layer class manager requires other JVMs to access a key for the stored class in order to synchronize access to the Java class among several installed and operating JVMs of the computer system. By sharing common Java classes in this fashion, the memory resource overhead required to operate multiple JVMs on a common computer system is drastically reduced thereby allowing a multiple JVM platform to be operable on an embedded computer system. A novel communication method is also disclosed for communicating information between a JVM application and a native application using the computer system's operating system. The novel communication method also allows multiple JVM applications to communicate using the shared memory pool. These functions are incorporated into a JavaLayer that supports the full PersonalJava TM platform.

French Abstract

L'invention concerne un procede et un systeme mis en oeuvre par ordinateur, permettant aux classes Java d'etre partagees par diverses machines virtuelles Java (JVMS) comprenant un systeme de communication

permettant aux applications natives d'interoperer. L'installation de JVM sur une plate-forme de systeme d'exploitation, telle qu'Aperios AV/OS, permet a diverses applications dont des applications de bureau, des mini-applications et des applications basees sur Internet, des applications de reseautage familial, des mini-applications MHEG-6, des jeux et des applications de jeu, et des application audiovisuelles de la prochaine generation, de fonctionner dans le systeme d'exploitation. L'invention concerne un pool de memoire partage (SMP) dans lequel un JVM peut memoriser et enregistrer une classe Java particuliere. La classe Java enregistree et memorisee est ensuite accessible a d'autres JVM au moyen du SMP et un regisseur de classe de couche Java installe dans le logiciel. Le regisseur de classe de couche Java necessite d'autres JVM pour acceder a un cle pour la classe memorisee, de sorte que soit synchronise l'acces a la classe Java parmi plusieurs JVM d'exploitation et installes du systeme informatique. En partageant ainsi des classes Java, le debit de ressources de memoire necessaire a l'exploitation de multiples JVM sur un systeme informatique commun est reduit sensiblement, ce qui permet a une plate-forme a JVM multiples d'etre exploitable sur un systeme informatique integre. Un nouveau procede de communication permettant la communication d'informations entre une application JVM et une application native, au moyen du systeme d'exploitation du systeme informatique est egalement decrit. Le nouveau procede de communication permet egalement a des applications a JVM multiples de communiquer au moyen du pool de memoire partage. Ces fonctions sont integrees a une couche Java qui prend en charge une plate-forme integrale PersonalJava .

Legal Status (Type, Date, Text)

Publication 20000908 A1 With international search report.

Publication 20000908 A1 Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

Fulltext Availability:

Detailed Description

Detailed Description

... and is accessible by every JVM object. As shown in Figure 5, Javalayer 160 supports **multiple JVMs** running for devices which have enough **memory** . Figure 6 shows the architecture which supports multiple applications running on one JVM 130 simultaneously for memory-limited... requesting JVM when the lock is free. The JVM can then access the shared Java **class** .

As **shown** in Figure 7, an application can be associated with **each JVM**

As shown, application 256 is associated with JVIVI 251, application 257 is associated with JVM...

17/5,K/37 (Item 37 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00236233 **Image available**

METHOD AND APPARATUS FOR MONITORING THE STATUS OF NON-POLLABLE DEVICES IN A COMPUTER NETWORK

PROCEDE ET DISPOSITIF SERVANT A CONTROLER L'ETAT DE DISPOSITIFS NON INTERROGEABLES DANS UN RESEAU INFORMATISE

Patent Applicant/Assignee:

CABLETRON SYSTEMS INC,

Inventor(s):

DEV Roger H,
NELSON Mark H,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9310495 A1 19930527

Application: WO 92US10066 19921120 (PCT/WO US9210066)

Priority Application: US 91797121 19911122

Designated States: AU JP AT BE CH DE DK ES FR GB GR IE IT LU MC NL SE

Main International Patent Class: G06F-011/32

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 11927

English Abstract

A network management system includes a user interface (10), a virtual network (12) and a device communication manager (14). The virtual network (12) includes models (130-135) which represent network entities (30-35) and model relations which represent relations between network entities. Each model (130-135) includes network data relating to a corresponding network entity (30-35) and one or more inference handlers for processing the network data to provide user information. The system can poll or communicate with certain network entities (510, 520, 530, 540, 550) and can infer the status of network connector and other entities (500) for which polling is impossible or impractical. The system performs a fault isolation technique wherein the fault status of the network device is suppressed when it is determined that the device is not defective. User displays include hierarchical location views and topological views of the network configuration. Network devices are represented on the displays by multifunction icons (330, 332, 334) which permit the user to select additional displays showing detailed information regarding different aspects of the corresponding network device.

French Abstract

Un systeme de gestion de reseau comprend une interface d'utilisateur (10), un reseau virtuel (12) et un module de gestion (14) des communications de dispositifs. Le reseau virtuel (12) comprend des modeles (130-135) representant des entites (30-35) du reseau, ainsi que des relations de modeles representant des relations entre les entites du reseau. Chaque modele (130-135) comprend des donnees de reseau relatives a une entite de reseau correspondante (30-35) et un ou plusieurs modules de deduction servant a traiter les donnees du reseau, afin de fournir des informations a l'utilisateur. Le systeme peut interroger certaines entites (510, 520, 530, 540, 550) du reseau, ou communiquer avec celles-ci et peut deduire l'etat du connecteur de reseau et d'autres entites (500) du reseau pour lesquelles l'interrogation est impossible ou difficile. Le systeme met en application une technique d'isolation de defaillances permettant de supprimer l'etat de defaillance d'un dispositif du reseau, quand il a ete determine que le dispositif n'est pas defectueux. Des ecrans d'affichage d'utilisateur comprennent des vues de localisations et des vues topologiques hierarchiques de la configuration du reseau. Les dispositifs du reseau sont representes sur les ecrans d'affichage par des symboles graphiques a fonctions multiples (330, 333, 334) permettant a l'utilisateur de selectionner des affichages supplementaires presentant des informations detaillees concernant differents aspects du dispositif du reseau correspondant.

Fulltext Availability:

Detailed Description

Detailed Description

... contains a description of the defined attribute, When the user asks for help regarding this **attribute** , the text string **appears** on the user interface **screen** , (6) **Attribute** value is the value of the attribute.

The models used in the **virtual network machine** also include one or more inference handlers. An inference handler in this embodiment is a...user clicks on an icon to proceed to another view., the icon manager determines the **view class** and the next **view** , The icon manager then issues a new view by passing the **view class** and the appropriate **virtual network machine** model ID to the view executive, thereby causing the current view to be destroyed.

File 347:JAPIO Nov 1976-2003/Dec(Updated 040402)
 (c) 2004 JPO & JAPIO
 File 350:Derwent WPIX 1963-2004/UD,UM &UP=200421
 (c) 2004 Thomson Derwent
 File 348:EUROPEAN PATENTS 1978-2004/Apr W01
 (c) 2004 European Patent Office
 File 349:PCT FULLTEXT 1979-2002/UB=20040408,UT=20040401
 (c) 2004 WIPO/Univentio

Set	Items	Description
S1	3	AU='FRERKING M J'
S2	10	AU='HARDIN D S'
S3	2	AU='HARDIN DAVID S'
S4	6	AU='MYKRIS N M'
S5	1	AU='WILEY P'
S6	5	AU='WILEY P J'
S7	2	AU='WILEY PHILIP JOHN'
S8	3	S1 AND S2:S7
S9	22985	VIRTUAL(1W)MACHINE? ? OR VM OR VMS OR JVM OR JVMS
S10	6	S1:S7 AND S9
S11	8	S8 OR S10

11/9/1 (Item 1 from file: 350)
 DIALOG(R)File 350:Derwent WPIX
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015544596 **Image available**
 WPI Acc No: 2003-606752/200357
 Related WPI Acc No: 2003-102939
 XRPX Acc No: N03-483775

JAVA virtual machine management system for processor system, suspends processing instructions of virtual machine, when virtual machine active period timer causes timer component to indicate virtual machine switch

Patent Assignee: AJILE SYSTEMS INC (AJIL-N)
 Inventor: **HARDIN D S** ; MASS A P; MASTERS M H; **MYKRIS N M**
 Number of Countries: 001 Number of Patents: 001
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20030101440	A1	20030529	US 2001262254	P	20010117	200357 B
			US 2001683336	A	20011214	
			US 2002157005	A	20020529	

Priority Applications (No Type Date): US 2001262254 P 20010117; US 2001683336 A 20011214; US 2002157005 A 20020529

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20030101440	A1	22	G06F-009/45	Provisional application US 2001262254

Cont of application US 2001683336

Abstract (Basic): US 20030101440 A1

NOVELTY - A timer component includes a **virtual machine** activation period time for timing active period of activated **virtual machines** (500,502). A processor suspends processing instructions of a **virtual machine** indicated to be the active **virtual machine**, when the **virtual machine** active period timer causes the timer component to indicate a **virtual machine** switch.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for **JAVA virtual machine** management method.

USE - JAVA virtual machine management system for processor system.

ADVANTAGE - The system facilitates to simultaneously run several virtual machines with full space and time protection, without interference of one of the application with memory space of the other application.

DESCRIPTION OF DRAWING(S) - The figure shows the JAVA virtual machine management system.

activated virtual machines (550,502)

pp; 22 DwgNo 5/10

Title Terms: VIRTUAL; MACHINE; MANAGEMENT; SYSTEM; PROCESSOR; SYSTEM; SUSPENSION; PROCESS; INSTRUCTION; VIRTUAL; MACHINE; VIRTUAL; MACHINE; ACTIVE; PERIOD; TIME; CAUSE; TIME; COMPONENT; INDICATE; VIRTUAL; MACHINE; SWITCH

Derwent Class: T01

International Patent Class (Main): G06F-009/45

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05G3; T01-F07; T01-N03B1

11/9/2 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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015138592 **Image available**

WPI Acc No: 2003-199118/200319

Related WPI Acc No: 2003-103007

XRPX Acc No: N03-158336

Hardware and software associating apparatus for software development, has dispatch thread component which creates software event to be output through dispatch thread output interface

Patent Assignee: AJILE SYSTEMS INC (AJIL-N)

Inventor: FRERKING M J ; HARDIN D S ; WILEY P J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020165999	A1	20021107	US 2001273858	P	20010307	200319 B
			US 200293952	A	20020307	

Priority Applications (No Type Date): US 2001273858 P 20010307; US 200293952 A 20020307

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

US 20020165999 A1 25 G06F-009/46 Provisional application US 2001273858

Abstract (Basic): US 20020165999 A1

NOVELTY - A first-in-first-out (FIFO) data structure has FIFO output interface, which is coupled to the hardware interrupt signal input interface. A dispatch thread component has an event input coupled with FIFO output and has a dispatch thread output interface. The dispatch thread component creates a software event to be output through dispatch thread output interface.

USE - For associating hardware interrupt event with software component event for software development of object oriented programs such as JAVA and C++.

ADVANTAGE - Hardware interrupt event and software component event is associated, reliably.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of the multicast transmission environment.

pp; 25 DwgNo 3/6

Title Terms: HARDWARE; SOFTWARE; ASSOCIATE; APPARATUS; SOFTWARE; DEVELOP;

DISPATCH; THREAD; COMPONENT; SOFTWARE; EVENT; OUTPUT; THROUGH; DISPATCH;
THREAD; OUTPUT; INTERFACE

Derwent Class: T01

International Patent Class (Main): G06F-009/46

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05A; T01-H05A; T01-J20A

11/9/3 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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015042491 **Image available**

WPI Acc No: 2003-103007/200309

Related WPI Acc No: 2003-199118

XRPX Acc No: N03-082326

Component model for time sensitive embedded software applications, has
event transmission object that conveys data describing event to listener
software component that has code to receive event notification

Patent Assignee: AJILE SYSTEMS INC (AJIL-N)

Inventor: FRERKING M J ; HARDIN D S ; WILEY P J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020163520	A1	20021107	US 2001273858	P	20010307	200309 B
			US 200293943	A	20020307	

Priority Applications (No Type Date): US 2001273858 P 20010307; US
200293943 A 20020307

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

US 20020163520 A1 26 G06T-001/00 Provisional application US 2001273858

Abstract (Basic): US 20020163520 A1

NOVELTY - An event transmission object (104) comprises primitive
data type parameter that conveys data describing an event generated by
the event generating software component (100), to the listener software
component (102) having code to receive event notification.

USE - For development of time sensitive embedded software
applications using object oriented computing languages such as C++,
JAVA, etc.

ADVANTAGE - Enables to create an appropriate event transmission
object for facilitating communication, reliably. Provides a system for
interfacing a hardware interrupt generating component with a software
event handling system.

DESCRIPTION OF DRAWING(S) - The figure shows the schematic view of
component model.

Software components (100,102)

Event transmission object (104)

pp; 26 DwgNo 1/6

Title Terms: COMPONENT; MODEL; TIME; SENSITIVE; EMBED; SOFTWARE; APPLY;
EVENT; TRANSMISSION; OBJECT; CONVEY; DATA; DESCRIBE; EVENT; LISTENER;
SOFTWARE; COMPONENT; CODE; RECEIVE; EVENT; NOTIFICATION

Derwent Class: T01

International Patent Class (Main): G06T-001/00

File Segment: EPI

Manual Codes (EPI/S-X): T01-F02A; T01-F07; T01-J20A

11/9/4 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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015042423 **Image available**

WPI Acc No: 2003-102939/200309

Related WPI Acc No: 2003-606752

XRPX Acc No: N03-082267

Multiple virtual machine interrupt management method in computer system, involves servicing interrupt signal of virtual machines with highest priority

Patent Assignee: AJILE SYSTEMS INC (AJIL-N)

Inventor: **HARDIN D S** ; MASS A P; MASTERS M H; **MYKRIS N M**

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020161961	A1	20021031	US 2001262254	P	20010117	200309 B
			US 2001683336	A	20011214	

Priority Applications (No Type Date): US 2001262254 P 20010117; US 2001683336 A 20011214

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
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US 20020161961	A1	22	G11C-005/00	Provisional application US 2001262254
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Abstract (Basic): US 20020161961 A1

NOVELTY - Several concurrently running independent **virtual machines** determine the interrupt signals with highest priority, for servicing the highest priority interrupt signal.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) Interrupt management system; and
- (2) Interrupt controller

USE - For managing interrupt of multiple **JAVA virtual machines** in computer system.

ADVANTAGE - By servicing the interrupt signal of **virtual machine** with highest priority, the processing overload of the computer system is determined accurately.

DESCRIPTION OF DRAWING(S) - The figure shows the multiple **virtual machine** management system and the external bus interface.

pp; 22 DwgNo 2, 3, 4/10

Title Terms: MULTIPLE; VIRTUAL; MACHINE; INTERRUPT; MANAGEMENT; METHOD; COMPUTER; SYSTEM; SERVICE; INTERRUPT; SIGNAL; VIRTUAL; MACHINE; HIGH; PRIORITY

Derwent Class: T01

International Patent Class (Main): G11C-005/00

File Segment: EPI

Manual Codes (EPI/S-X): T01-F02A1; T01-F05G3; T01-F07

11/9/5 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014835536 **Image available**

WPI Acc No: 2002-656242/200270

XRPX Acc No: N02-518701

Multiple JAVA virtual machine concurrent operation method for real-time application, involves determining whether JAVA virtual machine is active/inactive during assignment of virtual machine to partition of processor time

Patent Assignee: **HARDIN D S** (HARD-I); MASS A P (MASS-I); MASTERS M H (MAST-I); **MYKRIS N M** (MYKR-I); NGOC D L (NGOC-I)

Inventor: **HARDIN D S** ; MASS A P; MASTERS M H; **MYKRIS N M** ; NGOC D L

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020099753	A1	20020725	US 2001681136	A	20010120	200270 B

Priority Applications (No Type Date): US 2001681136 A 20010120

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20020099753	A1	15	G06F-017/00	

Abstract (Basic): US 20020099753 A1

NOVELTY - Each JAVA **virtual machine** is assigned to each partition of processor time. The **virtual machine** during its assigned partition operates on a single processor. The **virtual machine** is determined to be active/inactive during its assigned partition.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) Computing apparatus;
 - (2) Computer readable storage medium storing **virtual machine** ;
- and
- (3) Operation program.

USE - For operating multiple JAVA **virtual machine** concurrently using computer in real-time application.

ADVANTAGE - Energy is conserved and efficiency is improved.

DESCRIPTION OF DRAWING(S) - The figure shows the data structure of multiple concurrent **virtual machines** .

pp; 15 DwgNo 1/7

Title Terms: MULTIPLE; VIRTUAL; MACHINE; CONCURRENT; OPERATE; METHOD; REAL; TIME; APPLY; DETERMINE; VIRTUAL; MACHINE; ACTIVE; INACTIVE; ASSIGN; VIRTUAL; MACHINE; PARTITION; PROCESSOR; TIME

Derwent Class: T01

International Patent Class (Main): G06F-017/00

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05G3; T01-S03

11/9/6 (Item 6 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014806373 **Image available**

WPI Acc No: 2002-627079/200267

XRFX Acc No: N02-495986

Computer-implemented builder tool for multiple virtual machine system, has parameter detail component which displays parameter information associated with one of virtual machines of overview component

Patent Assignee: FRERKING M J (FRER-I); HARDIN D S (HARD-I); MYKRIS N M (MYKR-I); WILEY P J (WILE-I)

Inventor: **FRERKING M J ; HARDIN D S ; MYKRIS N M ; WILEY P J**

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020095396	A1	20020718	US 2001261925	A	20010116	200267 B
			US 2001681263	A	20010309	

Priority Applications (No Type Date): US 2001261925 P 20010116; US 2001681263 A 20010309

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes
US 20020095396 A1 41 G06F-007/00 Provisional application US 2001261925

Abstract (Basic): US 20020095396 A1

NOVELTY - An over view components presented in a display component depicts multiple **virtual machines** . A parameter detail component displays parameter information associated with one of the **virtual machines** of the overview component.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

(1) Target executable file creation method; and
(2) Computer readable storage medium storing target executable file creation program.

USE - For creating target executable file for multiple **virtual machine** environment, embedded environment and also for use with JAVA **virtual machines** , etc.

ADVANTAGE - Provides user friendly multiple **virtual machine** builder environment. Determines both the build information and various project and **virtual machine** parameter efficiently for multiple **virtual machine** environment. Efficiently manages allocations of resources among various **virtual machines** .

DESCRIPTION OF DRAWING(S) - The figure shows the various inputs to the builder tool for multiple **virtual machine** environment and resultant target executable file.

pp; 41 DwgNo 1/31

Title Terms: COMPUTER; IMPLEMENT; BUILD; TOOL; MULTIPLE; VIRTUAL; MACHINE; SYSTEM; PARAMETER; DETAIL; COMPONENT; DISPLAY; PARAMETER; INFORMATION; ASSOCIATE; ONE; VIRTUAL; MACHINE; COMPONENT

Derwent Class: T01

International Patent Class (Main): G06F-007/00

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05G3; T01-F07; T01-J20B; T01-S03

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DIALOG(R)File 350:Derwent WPIX

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014592938 **Image available**

WPI Acc No: 2002-413642/200244

XRPX Acc No: N02-325095

Java virtual machines running apparatus e.g. real-time embedded microprocessor, operates timers to enforce context switching from one JVM to master JVM and then to another JVM after predetermined period

Patent Assignee: ROCKWELL COLLINS INC (ROCW)

Inventor: GEE J K; GREVE D A; HARDIN D S ; MASS A P; MASTERS M H; MYKRIS N M ; WILDING M M

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6374286	B1	20020416	US 9856126	A	19980406	200244 B

Priority Applications (No Type Date): US 9856126 A 19980406

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 6374286	B1		40	G06F-009/52	

Abstract (Basic): US 6374286 B1

NOVELTY - An initiation mechanism creates data and control structures, for several java **virtual machines** (**JVMs**), for storage

in a memory and designates a **JVM** as a master **JVM** . A predetermined timer periodically forces a context switching from an operating **JVM** to the master. Another timer operates after a predetermined period, to cause context switching from the master to next **JVM** .

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for the following:

- (1) **JVMs** running method; and
- (2) Computer program product for running **JVMs** .

USE - Java **virtual machines** running apparatus e.g. real-time embedded microprocessor used in electronic system such as home appliance, toy, aircraft autopilot system, chemical processing controls and safety device and for deployment of fuel injection, anti-lock breaking, air bag, engine ignition in vehicles.

ADVANTAGE - Prevents a system failure due to malfunction in either the master **JVM** or another **JVM** by time intervals such as partition times and interstice times enforced by hardware timers. Handles system interrupts and housekeeping duties by employing master **JVM** .

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of internal hardware construction of the microprocessor.

pp; 40 DwgNo 2/20

Title Terms: VIRTUAL; MACHINE; RUN; APPARATUS; REAL; TIME; EMBED;

MICROPROCESSOR; OPERATE; TIME; ENFORCE; CONTEXT; SWITCH; ONE; MASTER;

AFTER; PREDETERMINED; PERIOD

Derwent Class: T01; W04

International Patent Class (Main): G06F-009/52

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05E; W04-X03E

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DIALOG(R)File 350:Derwent WPIX

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014219625 **Image available**

WPI Acc No: 2002-040323/200205

XRPX Acc No: N02-029807

Java embedded processor for microprocessor applications using an architecture optimized for Java program code execution in real time

Patent Assignee: ROCKWELL COLLINS INC (ROCW)

Inventor: GEE J K; GREVE D A; **HARDIN D S** ; HIRATZKA T D; KAMIN R A; MASS A P; MASTERS M H; **MYKRIS N M**

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6317872	B1	20011113	US 97890813	A	19970711	200205 B
			US 9856048	A	19980406	

Priority Applications (No Type Date): US 9856048 A 19980406; US 97890813 A 19970711

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6317872	B1	34	G06F-009/45	CIP of application US 97890813

Abstract (Basic): US 6317872 B1

NOVELTY - The real time embedded architecture is optimized to run the intermediate code generated by the Java **Virtual Machine** . Garbage collection is achieved by an indirect referencing technique which can be bypassed for objects necessary of real time operation.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for method of executing high level source.

USE - For home appliances, children's toys, aircraft autopilot systems, chemical processing control devices.

ADVANTAGE - Efficiently runs programs by reducing program development and program maintenance cost.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of microprocessor system.

pp; 34 DwgNo 1/17

Title Terms: EMBED; PROCESSOR; MICROPROCESSOR; APPLY; ARCHITECTURE; PROGRAM
; CODE; EXECUTE; REAL; TIME

Derwent Class: T01

International Patent Class (Main): G06F-009/45

File Segment: EPI

Manual Codes (EPI/S-X): T01-F05G3; T01-J08A; T01-M05; T01-S01B; T01-S01C

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